<u>REMARKS</u>

In the above-identified Office Action the pending claims were rejected as being obvious in view of a combination of the disclosures of the cited Kuwata and Iwasaki patents. By this response, however, independent Claim 17 has been extensively revised in a manner which is believed to provide patentable distinctions over those references.

In particular, the present invention may be characterized as providing serial to parallel conversion so that the reading of image data from a first FIFO is expedited and is executed at high speed. Accordingly, in the claimed structure wherein frames of the image data are successively written into the frame memory of a storage capacity of one frame, the necessary time can be obtained for then reading the image data from the frame memory.

The cited references do not disclose these features of Applicants' invention. Specifically, the Kuwata patent does not disclose the serial to parallel conversion of the present invention, and the Iwasaki patent, although it discloses serial to parallel conversion, uses that conversion only to improve writing speed, but fails to provide the claimed timing relationships wherein the time saved during the writing of data into the frame memory section can be used during the subsequent reading out of that data.

For all of these various reasons Applicants believe that independent Claim 17 and dependent Claim 18 are allowable, and the issuance of a formal Notice of Allowance is solicited.

Applicant's undersigned attorney may be reached in our New York Office by telephone at (212) 218-2100. All correspondence should continue to be directed to our address listed below.

Respectfully submitted,

Attorney for Applicant

John A. Krause

Registration No. 24,613

FITZPATRICK, CELLA, HARPER & SCINTO 30 Rockefeller Plaza
New York, New York 10112-3801
Facsimile: (212) 218-2200

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